



FACULTY DEVELOPMENT PROGRAMME (FDP)
On
SPEECH AND IMAGE PROCESSING
(10th to 15th April, 2017)



Organized by
MVR College of Engineering and Technology, Paritala (V), Kanchikacharla (M), Krishna
In Association with
National Institute of Technology, Warangal
Under the scheme of **Electronics & ICT Academy**
(Sponsored by Department of Electronics and Information Technology (DeitY), GOI)

Preamble:

“Electronics & ICT Academy” is being setup at NIT Warangal with financial assistance from DeitY, MCIT, Govt. of India (GoI). The jurisdiction of this academy is Telangana, Andhra Pradesh, Andaman and Nicobar Islands, Goa, Puducherry and Karnataka states/UTs. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies; Curriculum development for Industry; Training & Consultancy services for Industry; CEP for working professionals; Advice and support for technical incubation and entrepreneurial activities.

As part of this initiative, a Faculty Development Programme (FDP) on Speech and Image Processing is being conducted to the Faculty members of engineering colleges, industries and research laboratories.

Major Course Contents:

- **Nature of Speech Signal and Basics of Speech Processing**
- **Speech Production and Analysis**
- **Speech Enhancement and Recognition**
- **Speech Synthesis**
- **Overview of Image Processing: Image Transforms**
- **Image enhancement and Compression**
- **Image filtering in frequency domain**
- **Applications of Wavelets**
- **Image segmentation and Classification**
- **Research Trends in Speech and Image Processing**

Hands on session on the above topics will be provided.

Organized at:

The programme will be coordinated and conducted by Department of Electronics and Communication Engineering of MVR College of Engineering and Technology, Paritala, Krishna. The whole course is handled by academicians from premier institutions, Industry experts and Research advisors.

Eligibility:

The programme is open to the teachers of engineering colleges in Telangana, Andhra Pradesh, Andaman and Nicobar Islands, Goa, Puducherry and Karnataka states/UTs. Industry personnel working in the concerned/allied discipline can also attend

Registration Fee Particulars:

- **Faculty members and Research Scholars form mentioned states : Rs2,000/- Only**
- **Industry participants : Rs6,000/-Only**
- **Participants from other states : Rs8,500/-Only**
- **For SC/ST Participants : Rs500/-Only**

The participants need to send a crossed demand draft (DD) drawn in favor of “**Director, NIT Warangal**” and payable at SBH, NIT Warangal branch or remit the necessary course fee to the Bank as per the details given below.

Account Name	Electronics & ICT Academy NITW
Account Number	62423775910
Name	State Bank of Hyderabad
Branch	REC Warangal (NIT Campus)
Branch Code	20149
IFSC	SBHY0020149
MICR	506004011

Accommodation:

No TA/DA will be paid to the participants. Accommodation will be provided on prior request. Working Lunch, Tea & Snacks would be provided during the training in the campus.

How to Apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with DD/NEFT Receipt) should reach the coordinator by speed post. It is also mandatory to send scanned application form and DD/NEFT receipt through e-mail as selection will be intimated only through mail.

Selection Criteria:

Selection will be done based on **first-cum-first-serve basis** and the confirmed candidates will be notified immediately. The maximum number of participants will be **50(fifty)**. Additionally 10 participants from industry are allowed to participate. The list of selected participants will be notified in the institute website www.nitw.ac.in and also will be sent to their personal *e-mail* ids. In case a candidate is not selected, the demand draft will be sent back. A test will be conducted at the end of the course. Candidates will be issued certificates on successful completion of the course along with grade. Reservations are followed for selecting candidates as per **GOI** norms.

Important Dates:

Last date for submission of application : **31st March 2017**
Selection-list intimation/display before : **03rd April 2017**
Duration of Program : **10th to 15th April 2017**

About NIT, Warangal:

National Institute of Technology (formerly Regional Engineering College), Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institution imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programs in various specializations.

About MVR:

SWARNA ACADEMY OF SCIENCES, VIJAYAWADA (ACADEMY) was established in the year 2007 for the noble cause of imparting high quality technical education in Engineering, Technology, and Management sciences. The ACADEMY is promoted by Sri Muthavarapu Srinivasa Babu, a very dynamic, innovative and enterprising person, committed and dedicated to the cause of education. The MVRCOE is located at Paritala, 30 km from Vijayawada on Hyderabad highway and is spread over 50 acres of lush green land scape spotted with orchards and groves.

MVRCOE offers B.Tech Courses in ECE, CSE, MECH, EEE, CSE, Civil & PG courses M. Tech and MBA. All laboratories are equipped with state of the art facilities.

Coordinators:

Dr. T. Kishore Kumar

Associate Professor, Department of ECE
National Institute of Technology, Warangal.

Dr. P.V.Naganjaneyulu

Professor, Department of ECE

MVR College of Engineering and Technology, Paritala, Krishna.



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APPLICATION FORM

- Name of the Applicant:
- Designation :
- Institution:
- E-mail ID :
- Mobile:
- Category
- Payment Mode
- Address for Correspondence:
- Subjects taught so far:
- Educational Qualifications with specialization:
- No .of refresher courses/workshops attended:
- Experience (in years) : Teaching Research Industry
- Do you belong to reservations SC/ST (YES/NO): If YES, please specify ** Attach photo copy
- Do you require accommodation during Training : YES/NO

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place :

Date :

Signature of the Applicant

SPONSORSHIP CERTIFICATE

Dr./Mr./Ms.....is an employee of our Institute/Organization and is here by sponsored to participate in the FDP on Speech and Image Processing sponsored by **Electronics & ICT Academy** during 10th to 15th April 2017 at MVR College of Engineering and Technology, Paritala, Krishna.

Place :

Date :

Signature of Principal/Head of Institution

Address for correspondence

Post your application form along with DD/NEFT Receipt to

Dr. T. Kishore Kumar, Coordinator, ,
Department of ECE, NIT Warangal,
Warangal, Telangana State - 506 004. Krishna
Ph No: 8332969353(M) 0870 -2462440 (O)
Email: kishorefr@gmail.com

Or

Dr. P.V.Naganjaneyulu, Coordinator,
Department of ECE, MVR College of Engineering
and Technology, Paritala(V), Kanchikacharla(M),
Krishna (Dt.), Andhra Pradesh- 521 180.
Ph No: 8498037777(M) 8187827777(O)
Email: principal@mvrcoe.ac.in

For more details about Electronics and ICT academy please visit: <http://nitw.ac.in/eict/>