

MVR COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Govt. of A.P., Affiliated to JNTUK, Kakinada)

PARITALA, KANCHIKACHERLA - 521180, Krishna Dist. A.P.

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II ECE-A II SEMESTER ACADEMIC YEAR: 2022-2023

CLASS IN-CHARGE: V.SATISH BABU

w. e. f: 27-02-2023

DAY	09.00 09.50	09.50 10.40	10.40 10.50	10.50 11.40	11.40 12.30	12.30 1.20	1.20 2.05	2.05 2.50	3.00 3.45	3.45 4.30	
	1	2	B R E A K	3	4	L U N C H B R E A K	5	6		7	
MON	MEFA	MEFA		LICA	CV		AC/ ECA LAB				CV(T)
TUE	LICA	AC		MEFA	MEFA		ECA /AC LAB				AC(T)
WED	AC	LICA		CV	ECA		LICA/AURDINOLAB				MEFA(T)
THU	AC	CV		LICA	ECA		AURDINO/LICA LAB				ECA(T)
FRI	LICA			AC	ECA		CV	ECA	LICA(T)	SPORTS	
SAT	CV	AC			ECA		LICA	CV	SEMINOR	LIBRARY	

List of Faculty handling the subjects

II ECE-A			
S.NO	SUBJECT	NAME OF THE FACULTY	DEPT
1	Analog Communications	V.SATISH	ECE
2	Electronic circuit Analysis And Design	O.PRIYANK	ECE
3	Linear IC Applications	T.VENKATARATNAM	ECE
4	Complex variables	A.NAGALAKSHMI	S&H
5	Managerial Economics And Financial Analysis Complex Variables	K.NARASHIMA SWAMI	MBA
6	Analog Communications Lab	V.SATISH	ECE
7	Electronic circuit Analysis And Design Lab	CH.SREEDHER	ECE
8	Linear IC Applications Lab	T.VENKATARATNAM	ECE
9	Adriano (Skilled Developed Program)	R.SUNIL	ECE

V. Satish Babu

PRINCIPAL
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PARITALA, VIJAYAWADA-521 180