

MVR COLLEGE OF ENGINEERING & TECHNOLOGY
 (Approved by AICTE & Govt. of A.P., Affiliated to JNTUK, Kakinada)
 PARITALA, KANCHIKACHERLA – 521180, Krishna Dist. A.P.
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II ECE-B II SEMESTER ACADEMIC YEAR: 2022-2023

CLASS IN-CHARGE: T. VENKATARATNAM

w. e. f: 27-02-2023

DAY	09.00 09.50	09.50 10.40	10.40 10.50	10.50 11.40	11.40 12.30	12.30 1.20	1.20 2.05	2.05 2.50	3.00 3.45	3.45 4.30
	1	2	B R E A K	3	4	L U N C H B R E A K	5	6	7	8
MON	ECA	AC		MEFA	MEFA		LICA / AURDINO LAB			LICA(T)
TUE	MEFA	MEFA		LICA	AC		AURDINO/ LICA LAB			CV(T)
WED	CV	ECA		LICA	AC		AC/ECA LAB			AC(t)
THU	ECA	LICA		AC	CV		ECA/AC LAB			ECA(T)
FRI	AC	ECA		CV	LICA		CV	AC	SEMINOR	SPORTS
SAT	LICA	ECA		CV	LICA		AC	ECA	MEFA(T)	LIBRARY

List of Faculty handling the subjects

II ECE-B			
S.NO	SUBJECT	NAME OF THE FACULTY	DEPT
1	Analog Communications	Dr.U.YEDU KONDALU	ECE
2	Electronic circuit Analysis And Design	Dr.S.SRINIVASA REDDY	ECE
3	Linear IC Applications	Dr .S. BALAJI	ECE
4	Complex variables	A.NAGALAKSHMI	S&H
5	Managerial Economics And Financial Analysis Complex Variables	K.NARASHIMA SWAMI	MBA
6	Analog Communications Lab	D.RATNA KUMARI	ECE
7	Electronic circuit Analysis And Design Lab	Dr.S.SRINIVASA REDDY	ECE
8	Linear IC Applications Lab	Dr .S. BALAJI	ECE
9	Adriano (Skilled Developed Program)	R.SUNIL	ECE

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