

# MVR COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Govt. of A.P., Affiliated to JNTUK, Kakinada)

PARITALA, KANCHIKACHERLA - 521180, Krishna Dist. A.P.

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

## III ECE-(A) II SEMESTER ACADEMIC YEAR: 2022-2023

CLASS IN-CHARGE: Mr.R.SUNIL

DAY	09.00 09.50	09.50 10.40	10.40 10.50	10.50 11.40	11.40 12.30	12.30 01.20	01.20 02.05	02.05 02.50	02.50 03.00	03.00 03.45	03.45 04.30	
	1	2		3	4	LUNCH BREAK	5	6		7	8	
MON	FUEE	VLSI	BREAK	DSP	M&CC		MP&MC	VLSI/ MP&MC -LAB				
TUE	VLSI	MP&M C		FUEE	DSP		M&CC	A&A Programming/ VLSI				
WED	M&CC	FUEE		DSP	VLSI		MPMC	SEMIN AR	BREAK	SPORTS		
THU	DSP	M&CC		VLSI	MP&MC		FUEE	MP&MC/DSP-LAB				
FRI	M&CC	DSP		MP&MC	VLSI		FUEE	DSP /A&A PROGRAMMING-LAB				
SAT	MP&M C	VLSI		DSP	M&CC		FUEE	RM	BREAK	LIB		

### List of Faculty handling the subjects

III ECE			
S.NO	SUBJECT	NAME OF THE FACULTY	DEPT
1	Microprocessor and Microcontrollers	CH.SREEDHAR	ECE
2	VLSI Design	S.INDRA GANDHI	ECE
3	Digital Signal Processing	K.SATYANARAYANA	ECE
4	Mobile & Cellular Communication	O. MOUNIKA	ECE
5	Fundamentals of utilization of electrical energy	M.ASHOK	EEE
6	Microprocessor and Microcontrollers - Lab	CH.SREEDHAR	ECE
7	VLSI Design Lab	S.INDRA GANDHI	ECE
8	Digital Signal Processing Lab	K.SATYANARAYANA	ECE
9	ARM based/ Arduino based Programming	S.PHATAN KHAN	ECE
10	Research Methodology	Dr R.SAI KUMAR	ECE

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