

MVR COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Govt. of A.P., Affiliated to JNTUK, Kakinada)

PARITALA, KANCHIKACHERLA – 521180, Krishna Dist. A.P.

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

III ECE-(B)II SEMESTER ACADEMIC YEAR: 2021-2022

CLASS IN-CHARGE: K.SATYANARYANA

w. e. f: 01.08.2022

| DAY | 09.00 09.50 | 09.50 10.40 | 10.40 10.50 | 10.50 11.40 | 11.40 12.30 | 12.30 01.20 | 01.20 02.05 | 02.05 02.50 | 02.50 03.00 | 03.00 03.45 | 03.45 04.30 | |
|-----|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------------------------|----------------|----------------|----------------|--|
| | 1 | 2 | | 3 | 4 | LUNCH BREAK | 5 | 6 | | 7 | 8 | |
| MON | VLSI | FUEE | BREAK | M&CC | DSP | | M&CC | Programming/ VLSI | | | | |
| TUE | FUEE | DSP | | VLSI | MP&MC | | MPMC | VLSI/ MP&MC -LAB A&A | | | | |
| WED | DSP | VLSI | | M&CC | FUEE | | M&CC | SEMIN AR | BREAK | SPORTS | | |
| THU | VLSI | DSP | | M&CC | FUEE | | MP&MC | MP&MC/DSP-LAB | | | | |
| FRI | MP&M C | M&CC | | DSP | FUEE | | VLSI | DSP /A&A PROGRAMMING-LAB | | | | |
| SAT | VLSI | MP&M C | | M&CC | FUEE | | DSP | RM | BREAK | LIB | | |

List of Faculty handling the subjects

| III ECE | | | |
|---------|--|---------------------|------|
| S.NO | SUBJECT | NAME OF THE FACULTY | DEPT |
| 1 | Microprocessor and Microcontrollers | Dr. K.MURALI BABU | ECE |
| 2 | VLSI Design | Dr. S. VENKTESAN | ECE |
| 3 | Digital Signal Processing | C.SHYAM | ECE |
| 4 | Mobile & Cellular Communication | S.SADIKUNNISA | ECE |
| 5 | Fundamentals of utilization of electrical energy | M.ASHOK | EEE |
| 6 | Microprocessor and Microcontrollers - Lab | Dr. K.MURALI BABU | ECE |
| 7 | VLSI Design Lab | SHARANYA | ECE |
| 8 | Digital Signal Processing Lab | SRAVYA SRI | ECE |
| 9 | ARM based/ Arduino based Programming | .MOUNIKA | ECE |
| 10 | Research Methodology | Dr U.YEDU KONDALU | ECE |

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